

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

1. (original) In a communications network, a method for combining data packets intended for a common communications device, comprising:

    sorting data packets received during a predetermined time period into groups according to for which communications device of said network the received data packets are intended;

    respectively time aligning the data packets in each of the groups; and  
    orthogonally combining the sorted and time aligned data packets within each group.

2. (original) The method of claim 1, wherein said received data packets are sorted using a MAC header of each of said received data packets.

3. (original) The method of claim 1, wherein said received data packets are stored in different sections of a memory according to for which communications device of said network the received data packets are intended.

4. (original) The method of claim 1, further comprising sorting for transmission said orthogonally combined data packets in different sections of a memory according to for which communications device said combined data packets are intended.

5. (original) The method of claim 4, wherein said orthogonally combined data packets are stored in different sections of a memory according to which communications device of said network the combined data packets are to be transmitted.

6. (original) The method of claim 1, wherein said orthogonally combined data packets are transmitted to an intended receiver using a single MAC header.
7. (original) The method of claim 1, wherein a respective bandwidth required to transmit each group of said orthogonally combined data packets is substantially the same as a bandwidth required to transmit a largest data packet in each of said groups.
8. (original) The method of claim 1, wherein said predetermined time period is substantially greater than or equal to a total time latency for receiving data packets intended for a common communications device of said network.
9. (original) The method of claim 8, wherein data packets in said network are communicated according to a global timing schedule and said time latency is due to differences in the latencies of transmission media of the communications devices of said network.
10. (original) The method of claim 1, wherein only data packets having specific MAC headers are orthogonally combined.
11. (original) The method of claim 1, wherein data packets not orthogonally combined are communicated in said network according to conventional Ethernet protocols.
12. (original) An apparatus for combining data packets intended for a common communications device in a communications network, comprising:
  - a timer for defining a time period for receiving data packets;
  - an addressing device for defining a storage location for said received data packets according to for which communications device of said network the received data packets are intended;

a memory for storing said received data packets in different sections according to the storage location defined by said addressing device, wherein the data packets stored within each of said different sections are respectively timed aligned; and

a combiner for orthogonally combining the respective time aligned data packets in each of said different sections of said memory.

13. (original) The apparatus of claim 12, further comprising a second memory for storing said orthogonally combined data packets in different sections according to which communications device of said network the combined data packets are to be transmitted.

14. (original) The apparatus of claim 12, further comprising a bit scaler for defining the number of bits to be combined by said combiner.

15. (original) The apparatus of claim 12, wherein said addressing device stores information regarding the MAC header of which data packets are to be orthogonally combined.

16. (original) The apparatus of claim 12, further comprising a Receive MAC for receiving data packets and a Transmit MAC for transmitting the respective orthogonally combined data packets to an intended communications device.

17. (original) The apparatus of claim 12, wherein said apparatus is implemented in an interconnect switch of said network.

18. (original) The apparatus of claim 12, wherein said apparatus is implemented in at least one of the communications devices of said network.

19. (original) A packet network where data packets intended for a common communications device are combined, comprising:

a plurality of communications devices; and

a switch for interconnecting said communications devices, wherein said interconnection switch includes:

a timer for defining a time period for receiving data packets;

an addressing device for defining a storage location for said received data packets according to for which communications device of said network the received data packets are intended;

a memory for storing said received data packets in different sections according to the storage location defined by said addressing device, wherein the data packets stored within each of said different sections are respectively timed aligned; and

a combiner for orthogonally combining the respective time aligned data packets in each of said different sections of said memory.

20. (original) A packet network where data packets intended for a common communications device are combined, comprising:

a non-blocking switch for interconnecting communications devices of said network; and

a plurality of communications devices, wherein at least one of said communications devices includes:

a timer for defining a time period for receiving data packets;

an addressing device for defining a storage location for said received data packets according to for which communications device of said network the received data packets are intended;

a memory for storing said received data packets in different sections according to the storage location defined by said addressing device, wherein the data packets stored within each of said different sections are respectively timed aligned; and

a combiner for orthogonally combining the respective time aligned data packets in each of said different sections of said memory.